A Scalable Parallel PWTD-Accelerated SIE Solver for Analyzing Transient Scattering From Electrically Large Objects

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Abstract—A scalable parallel plane-wave time-domain (PWTD) algorithm for efficient and accurate analysis of transient scattering from electrically large objects is presented. The algorithm produces scalable communication patterns on very large numbers of processors by leveraging two mechanisms: 1) a hierarchical parallelization strategy to evenly distribute the computation and memory loads at all levels of the PWTD tree among processors and 2) a novel asynchronous communication scheme to reduce the cost and memory requirement of the communications between the processors. The efficiency and accuracy of the algorithm are demonstrated through its applications to the analysis of transient scattering from a perfect electrically conducting (PEC) sphere with a diameter of 70 wavelengths and a PEC square plate with a dimension of 160 wavelengths. Furthermore, the proposed algorithm is used to analyze transient fields scattered from realistic airplane and helicopter models under high frequency excitation.

Index Terms—Fast algorithms, marching-on-in-time (MOT), parallelization, plane-wave time-domain (PWTD) algorithm, time-domain surface integral equation (TD-SIE), transient scattering, very large-scale problems.

I. INTRODUCTION

RANSIENT electromagnetic scattering from complex objects involving perfect electrically conducting (PEC) surfaces and piecewise inhomogeneous dielectric volumes can be analyzed by solving time-domain surface integral equations (TD-SIEs) [1]. Development of multilevel plane-wave time-domain (PWTD) algorithm has enabled marching-on-intime (MOT) scheme a viable method for efficiently solving TD-SIEs [2]. The resulting PWTD-accelerated MOT-TD-SIE solvers have been shown to successfully compete with finite difference time-domain schemes in analyzing transient fields

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scattered from electrically large (i.e., many wavelengths long) real-life objects [3], [4].

The PWTD-accelerated MOT-TD-SIE solvers are the timedomain counterparts of multilevel fast multipole (MLFMA)accelerated frequency-domain SIE solvers. Their computational cost and memory requirement scale as $O(N_t N_s \log^2 N_s)$ and $O(N_s^{1.5})$, respectively. Here, N_s is the number of spatial unknowns and $N_t = O(N_s^{0.5})$ is the number of time steps. A comparison of these scaling estimates to those for the cost and memory requirements of unaccelerated MOT-TD-SIE solvers, which scale as $O(N_t N_s^2)$ and $O(N_s^2)$, respectively, demonstrates the benefits of multilevel PWTD acceleration. Having said that, the applicability of PWTD-accelerated MOT-TD-SIE solver in analyzing transient scattering from electrically large real-life objects can further be increased through effective parallelization schemes. Unfortunately, parallelizing multilevel PWTD algorithm on distributed memory clusters is a challenging task due to its complex nature and heterogeneous structure. Until recently, similar difficulties prevented efficient and scalable parallelization of MLFMA-accelerated frequency-domain SIE solvers [5].

Previously, schemes, which make use of spatial [6] and hybrid spatial/angular partitioning [7] strategies to efficiently parallelize the multilevel PWTD algorithm, were proposed. The PWTD algorithm with hybrid partitioning was incorporated into a TD-SIE solver and used for analyzing scattering from objects discretized with half a million spatial unknowns [7]. Unfortunately, neither of these parallelization schemes scales well on distributed memory clusters with large numbers of processors since their computational and communication costs and memory requirements per processor are not inversely proportional to the total processor count. This is expected since they make use of straight-forward time-domain extensions of spatial [8] and hybrid spatial/angular [9] partitioning schemes that were originally developed for parallelizing the MLFMA and are known to not scale well. The first provably scalable techniques for parallelizing the MLFMA did not appear in the archival literature until 2008 [10]-[14]. These techniques make use of a hierarchical partitioning strategy that simultaneously leverages spatial and angular partitioning at each level of the MLFMA tree [10]-[14].

Unfortunately, the direct extension of these MLFMA parallelization strategies to time domain does not produce a scalable parallel PWTD algorithm. This is simply because of the two important differences between MLFMA and PWTD:

1) MLFMAs call for only spatial and angular discretizations, while PWTD schemes require spatial, angular, and temporal discretizations. This results in different CPU and memory requirements for PWTD schemes. More specifically, from a parallelization perspective, communication costs in the translation stage of the PWTD schemes scale significantly different from those of the MLFMAs. 2) MLFMAs often use local (in angular dimension) schemes to interpolate and filter/anterpolate far-fields, while PWTD implementations, for reasons relating to stability of the TD-SIE solvers, usually rely on exact global (in angular dimension) spherical interpolation/filtering schemes [4]. Load balancing these global schemes poses challenges, especially at the coarse PWTD levels involving many plane wave directions.

These differences between MLFMA and PWTD listed above clearly motivate the formulation and implementation of a scalable scheme to parallelize the multilevel PWTD algorithm. This paper describes such a scheme that makes use of a hierarchical parallelization strategy to quasi-optimally distribute computation and memory loads pertinent to spatial, angular, and temporal dimensions among processors. In addition, a novel asynchronous communication technique for reducing the cost and memory requirements of the communications at the translation stage of the PWTD algorithm is developed. By combining the hierarchical partitioning strategy and this asynchronous communication technique to achieve CPU and memory load balancing among processors, the proposed scheme produces scalable communication patterns among processors at all levels of the PWTD tree. The resulting parallel PWTD algorithm is incorporated into a TD-SIE solver to enable efficient and accurate analysis of transient scattering from electrically large PEC objects. Indeed, numerical results demonstrate that the proposed parallel PWTD-accelerated TD-SIE solver can be efficiently applied to real-life electromagnetics problems involving scatterers spanning over a 100 wavelengths and discretized with 10 million spatial unknowns.

II. FORMULATION

A. TD-SIEs and MOT Scheme

Let S denote an arbitrarily shaped (closed) PEC surface that resides in free space with permittivity ε_0 and permeability μ_0 . S is excited by the incident electromagnetic field $\{\mathbf{E}^i(\mathbf{r},t),\mathbf{H}^i(\mathbf{r},t)\}$. $\mathbf{E}^i(\mathbf{r},t)$ and $\mathbf{H}^i(\mathbf{r},t)$ are assumed essentially bandlimited to maximum frequency f_{max} and vanishingly small $\forall \mathbf{r} \in S$ for t < 0. In response to the excitation, the surface current density $\mathbf{J}(\mathbf{r},t)$ is induced on S and $\mathbf{J}(\mathbf{r},t)$ generates the scattered field $\{\mathbf{E}^s(\mathbf{r},t),\mathbf{H}^s(\mathbf{r},t)\}$. $\mathbf{J}(\mathbf{r},t)$ satisfies the time-domain electric field and magnetic field integral equations (TD-EFIE and TD-MFIE) [15]

$$\hat{\mathbf{n}} \times \hat{\mathbf{n}} \times \partial_t \mathbf{E}^i(\mathbf{r}, t) = -\hat{\mathbf{n}} \times \hat{\mathbf{n}} \times \partial_t \mathbf{E}^s(\mathbf{r}, t)$$

$$= \mathcal{L}_e[\mathbf{J}](\mathbf{r}, t) \quad \forall \mathbf{r} \in S, S^+, S^- \qquad (1)$$

$$\hat{\mathbf{n}} \times \partial_t \mathbf{H}^i(\mathbf{r}, t) = -\hat{\mathbf{n}} \times \partial_t \mathbf{H}^s(\mathbf{r}, t)$$

$$= \mathcal{L}_h[\mathbf{J}](\mathbf{r}, t) \quad \forall \mathbf{r} \in S^-. \qquad (2)$$

Here, S^- and S^+ denote surfaces conformal to but just inside and outside S, $\hat{\mathbf{n}}$ denotes the outward pointing unit normal to S, and ∂_t represents the time derivative. The TD-EFIE and TD-MFIE operators \mathcal{L}_e and \mathcal{L}_h are

$$\mathcal{L}_{e}[\mathbf{J}](\mathbf{r},t) = \hat{\mathbf{n}} \times \hat{\mathbf{n}} \times \frac{\mu_{0}}{4\pi} \int_{S} dS' (\partial_{t}^{2} \mathcal{I} - c_{0}^{2} \nabla \nabla) \cdot \frac{\mathbf{J}(\mathbf{r}',\tau)}{R}$$
(3)

$$\mathcal{L}_{h}[\mathbf{J}](\mathbf{r},t) = \frac{1}{4\pi}\hat{\mathbf{n}} \times \int_{S} dS'(\mathbf{r} - \mathbf{r}')$$

$$\times \left[\frac{1}{c_{0}R^{2}} \partial_{t}^{2} \mathbf{J}(\mathbf{r}',\tau) + \frac{1}{R^{3}} \partial_{t} \mathbf{J}(\mathbf{r}',\tau) \right]$$
(4)

where \mathcal{I} is the identity dyad, $R = |\mathbf{r} - \mathbf{r}'|$ is the distance between source point \mathbf{r}' and observer point \mathbf{r} , $c_0 = 1/\sqrt{\varepsilon_0\mu_0}$ is the speed of light in free space, and $\tau = t - R/c_0$ denotes retarded time. The TD-EFIE applies to both open and closed surfaces [1]. When applied to closed surfaces, solutions to both the TD-EFIE and TD-MFIE are corrupted by oscillating currents that (approximately) reside in the null spaces of \mathcal{L}_e and \mathcal{L}_h [15], [16]. The time domain combined field integral equation (TD-CFIE) eliminates these spurious currents by linearly combining the TD-EFIE and TD-MFIE as

$$\hat{\mathbf{n}} \times \partial_t \mathbf{H}^i(\mathbf{r}, t) - \beta/\eta_0 \hat{\mathbf{n}} \times \hat{\mathbf{n}} \times \partial_t \mathbf{E}^i(\mathbf{r}, t)$$

$$= \mathcal{L}_h[\mathbf{J}](\mathbf{r}, t) - \beta/\eta_0 \mathcal{L}_e[\mathbf{J}](\mathbf{r}, t)$$

$$= \mathcal{L}_c[\mathbf{J}](\mathbf{r}, t) \quad \forall \mathbf{r} \in S^-. \tag{5}$$

Here, $\eta_0 = \sqrt{\mu_0/\varepsilon_0}$ is the wave impedance in free space. The TD-CFIE reduces to the TD-EFIE and TD-MFIE when $\beta = \infty$ and $\beta = 0$, respectively.

To numerically solve (5), $\mathbf{J}(\mathbf{r},t)$ is discretized using spatial basis functions $\mathbf{S}_n(\mathbf{r})$, $n=1,\ldots,N_s$, and temporal basis functions $T_i(t)$, $i=1,\ldots,N_t$

$$\mathbf{J}(\mathbf{r},t) = \sum_{n=1}^{N_s} \mathbf{S}_n(\mathbf{r}) f_n(t) = \sum_{n=1}^{N_s} \sum_{i=1}^{N_t} I_{n,i} \mathbf{S}_n(\mathbf{r}) T_i(t).$$
 (6)

Here, $f_n(t)$ is the temporal signature associated with $\mathbf{S}_n(\mathbf{r})$ and $I_{n,i}$ is the expansion coefficient associated with spacetime basis function $\mathbf{S}_n(\mathbf{r})T_i(t)$. The temporal basis function $T_i(t) = T(t-i\Delta t)$ is a shifted Lagrange interpolant [17] that is nonzero for $t > -\Delta t$; $\Delta t = 1/(2\chi_t f_{max})$ is the time step size and $\chi_t > 1$ is the temporal oversampling factor. The spatial basis functions $\mathbf{S}_n(\mathbf{r})$ are Rao–Wilton–Glisson (RWG) functions [18]. Substituting (6) into (5) and testing the resulting equation with $\mathbf{S}_m(\mathbf{r})$, $m=1,\ldots,N_s$, at $t=j\Delta t$ yields the set of linear equations

$$\bar{\bar{\mathbf{Z}}}_0 \bar{\mathbf{I}}_j = \bar{\mathbf{F}}_j - \sum_{i=1}^{j-1} \bar{\bar{\mathbf{Z}}}_i \bar{\mathbf{I}}_{j-i}.$$
 (7)

Here, the entries of the vectors $\bar{\mathbf{I}}_j$ and $\bar{\mathbf{F}}_j$ and the matrices $\bar{\mathbf{Z}}_i$ are

$$\{\bar{\mathbf{I}}_i\}_n = I_{n,i}, \ n = 1, \dots, N_s$$
 (8)

$$\{\bar{\mathbf{F}}_{j}\}_{m} = \langle \mathbf{S}_{m}(\mathbf{r}), \, \hat{\mathbf{n}} \times \partial_{t} \mathbf{H}^{i}(\mathbf{r}, t) -\beta/\eta_{0} \hat{\mathbf{n}} \times \hat{\mathbf{n}} \times \partial_{t} \mathbf{E}^{i}(\mathbf{r}, t) \rangle \big|_{t=j\Delta t}, \quad m = 1, \dots, N_{s}$$
(9)

$$\{\bar{\bar{\mathbf{Z}}}_i\}_{mn} = \langle \mathbf{S}_m(\mathbf{r}), \mathcal{L}_c[\mathbf{S}_n T_{-i}](\mathbf{r}, t) \rangle |_{t=0}, \quad m, n = 1, \dots, N_s$$
(10)

where $\langle \cdot, \cdot \rangle$ denotes the standard spatial inner product. The set of linear equations (7) can be solved by MOT: first, $\overline{\mathbf{I}}_1$ is computed by solving (7) for j=1. Then, for j=2, the summation on the right-hand side (RHS) of (7) is computed and resulting system is solved for $\overline{\mathbf{I}}_2$. This process is repeated to compute $\overline{\mathbf{I}}_3$ and so on. At every time step, an iterative solver is used to efficiently solve (7) for $\overline{\mathbf{I}}_j$. At a given time step, the computationally most demanding operation is the evaluation of the sum on the RHS of (7). This computation requires $O(N_t N_s^2)$ operations for all N_t time steps and $O(N_s^2)$ memory. Classical MOT schemes, therefore, are prohibitively expensive when applied to the analysis of transient scattering from electrically large structures. The multilevel PWTD algorithm significantly reduces the computational cost of the MOT scheme [4] and is summarized next.

B. Multilevel PWTD Algorithm

Consider a rectangular box enclosing S; it is recursively subdivided into eight boxes until the dimensions of the smallest boxes are on the order of the wavelength at the maximum frequency, $\lambda = c_0/f_{max}$. This recursive subdivision strategy gives rise to an N_L -level PWTD tree with levels labeled $v=1,\ldots,N_L=O\left(\log(N_s^{0.5})\right)$. The tree's finest level (v=1) contains the smallest boxes while its coarsest level $(v=N_L)$ contains the box enclosing S. Let N_g^v denote the number of nonempty boxes at level v. For (nonfractal) surface scatterers $N_g^1=O(N_s)$ and $N_g^{v+1}\approx N_g^v/4$. The radius of a sphere enclosing a level v box is $R^v=2^{(v-1)}R^1$ with $R^1=O(1)$.

Upon constructing the PWTD tree, far-field box pairs at each level are identified starting with level N_L-2 . Two nonempty boxes at level v are labeled a level v far-field pair if the distance between their centers is greater than γR^v ($3 \le \gamma \le 6$) and their respective parent boxes do not constitute a far-field pair [2]. Two nonempty boxes at the finest level, which are not labeled as a far-field pair are considered as a near-field pair; also each nonempty box at the finest level forms a near-field pair with itself. Interactions between spatial basis functions residing in near-field box pairs are computed using (10) and their contributions are directly added to the RHS of (7). Interactions between spatial basis functions contained in far-field box pairs are evaluated by the PWTD scheme.

Let α and α' denote a far-field box pair's source and observer boxes. Let $R_{c,\alpha\alpha'} = |\mathbf{R}_{c,\alpha\alpha'}| = |\mathbf{r}_o^c - \mathbf{r}_s^c|$ denote the distance between the source and observer box centers \mathbf{r}_s^c and \mathbf{r}_o^c . The source and observer boxes contain spatial basis functions $\mathbf{S}_n(\mathbf{r}), n \in \alpha$, and $\mathbf{S}_m(\mathbf{r}), m \in \alpha'$, respectively. For $\forall n \in \alpha$, the temporal signature $f_n(t)$ associated with $\mathbf{S}_n(\mathbf{r})$ is broken into N_l^v consecutive subsignals, $f_n^l(t), l = 1, \ldots, N_l^v$, using bandlimited local interpolants [19]. The subsignal $f_n^l(t)$ is bandlimited to $f_s = \chi_t f_{max}$ and has quasi-finite duration

 $T^v \Delta t \leq (R_{c,\alpha\alpha'} - 2R^v)/c_0$, with $T^v N_l^v \approx N_t$. Fields due to $\mathbf{S}_n(\mathbf{r}) f_n^l(t)$, $n \in \alpha$ tested by $\mathbf{S}_m(\mathbf{r})$, $m \in \alpha'$ are expressed as

$$\langle \mathbf{S}_{m}(\mathbf{r}), \mathcal{L}_{c}[\mathbf{S}_{n}f_{n}^{l}](\mathbf{r}, t) \rangle$$

$$= \frac{1}{8\pi^{2}c_{0}^{2}} \sum_{q=0}^{K^{v}} \sum_{p=-K^{v}}^{K^{v}} \omega_{qp}^{v} [-\beta \mathbf{P}_{m}^{-}(\hat{\mathbf{k}}_{qp}^{v}, t, \hat{\mathbf{k}}_{qp}^{v})$$

$$+ \mathbf{P}_{m}^{-}(\hat{\mathbf{k}}_{qp}^{v}, t, \hat{\mathbf{n}})]^{\dagger} * \mathcal{T}(\hat{\mathbf{k}}_{qp}^{v}, t) * \mathbf{P}_{n}^{+}(\hat{\mathbf{k}}_{qp}^{v}, t, \hat{\mathbf{k}}_{qp}^{v}) * f_{n}^{l}(t)$$
(11)

where $K^v = \lfloor 4\pi f_s \chi_s R^v/c_0 \rfloor + 1$ represents the number of spherical harmonics effectively accounted for in the plane wave expansions, χ_s represents the spherical oversampling factor, ω^v_{qp} are quadrature weights on the unit sphere $\hat{\mathbf{k}}^v_{qp}$, $q = 0, \ldots, K^v$ and $p = -K^v, \ldots, K^v$ represent directions of outgoing/incoming rays with a total of $N^v_k = (K^v + 1)(2K^v + 1)$ directions [2], \dagger denotes the transpose, and * represents time convolution. The projection function $\mathbf{P}^{\pm}_{\{m,n\}}(\hat{\mathbf{k}}^v_{qp},t,\hat{\mathbf{v}})$ is

$$\mathbf{P}_{\{m,n\}}^{\pm}(\hat{\mathbf{k}}_{qp}^{v}, t, \hat{\mathbf{v}}) = \int_{S_{\{m,n\}}} dS' \hat{\mathbf{v}} \times \mathbf{S}_{\{m,n\}}(\mathbf{r}') \delta(t \pm \hat{\mathbf{k}}_{qp}^{v} \cdot (\mathbf{r}' - \mathbf{r}_{\{o,s\}}^{c})/c_{0}).$$
(12)

The translation function $\mathcal{T}(\hat{\mathbf{k}}_{an}^v, t)$ is

$$\mathcal{T}(\hat{\mathbf{k}}_{qp}^{v}, t) = \frac{c_0 \partial_t^3}{2R_{c,\alpha\alpha'}} \sum_{k=0}^{K^v} (2k+1) \Phi_k \left(\frac{c_0 t}{R_{c,\alpha\alpha'}}\right) \times \Phi_k$$
$$\left(\frac{\hat{\mathbf{k}}_{qp}^{v} \cdot \mathbf{R}_{c,\alpha\alpha'}}{R_{c,\alpha\alpha'}}\right)$$
(13)

where $\Phi_k(\cdot)$ is the Legendre polynomial of degree k and $|t| \leq R_{c,\alpha\alpha'}/c_0$.

The PWTD algorithm is executed as follows. First, outgoing rays for all directions $\hat{\mathbf{k}}_{qp}^v$ are constructed by convolving the projection function $\mathbf{P}_n^+(\hat{\mathbf{k}}_{qp}^v,t,\hat{\mathbf{k}}_{qp}^v)$ with the subsignal $f_n^l(t)$. Next, outgoing rays in box α are convolved with $\mathcal{T}(\hat{\mathbf{k}}_{qp}^v,t)$ and are translated into incoming rays in box α' . Finally, the incoming rays are projected onto testing basis function $S_m(\mathbf{r})$ by convolving the projection function $\mathbf{P}_m^-(\hat{\mathbf{k}}_{qp}^v,t,\hat{\mathbf{k}}_{qp}^v)$ and $\mathbf{P}_m^-(\hat{\mathbf{k}}_{qp}^v,t,\hat{\mathbf{n}})$ with the incoming rays and summing over all directions with quadrature weights ω_{qp}^v [2]. Note that only outgoing/incoming rays of the finest level boxes are constructed/projected directly from/onto basis functions using the projection function (12); those of higher level boxes are constructed/projected by an exact global vector spherical filtering technique described in [4]. The analysis in [4] showed that the computational cost and memory requirements of a multilevel PWTD-accelerated MOT scheme applied to surface scatterers scale as $O(N_t N_s \log^2 N_s)$ and $O(N_s^{1.5})$, respectively.

C. Parallelization of the PWTD-Accelerated TD-SIE Solver

This section describes a highly scalable scheme for parallelizing the multilevel PWTD algorithm briefly described in the previous section. The proposed strategy leverages hierarchical

partitioning of the multilevel PWTD tree among processors and an asynchronous scheme for memory and cost efficient communications between processors. This asynchronous scheme is implemented using message passing interface (MPI). In what follows, first, an overview of the proposed parallelization strategy is provided (Section II-C1). Then, the costs estimates for computations and communications required at different stages of the PWTD algorithm are derived (Sections II-C2–II-C4). Finally, overall computational and communication costs of the proposed parallelization strategy are provided and its scalability is theoretically proven (Section II-C5).

1) A Pedestrian Description: The effective parallelization of the multilevel PWTD scheme calls for a uniform distribution of the near-field MOT matrix elements (i.e., near-field data) and outgoing/incoming rays (i.e., ray data) and the pertinent workload among processors. The near-field data can be uniformly distributed among processors in a straightforward manner. That said, distributing the ray data uniformly among processors is a challenging task due to the PWTD algorithm's heterogeneous tree structure. That is, ray data at level v of the PWTD tree are computed for $N_q^v = O(N_s/4^v)$ boxes (or spatial samples), $N_k^v = O(4^v)$ angular samples and approximately $T^v = O(2^v)$ temporal samples at each PWTD stage and its partitioning along a single dimension results in poor load balance and/or congested communications at certain levels. This problem is observed with spatial partitioning at higher levels and angular or temporal partitioning at lower levels. A viable solution to this problem is adaptively partitioning ray data along more than one dimension.

To achieve this, the proposed scheme first identifies a "base" level v_b at which the number of boxes $N_g^{v_b}$ is no less than the number of processors N_p . For simplicity, v_b is chosen as the highest possible such level. It should be noted here that the proposed partitioning strategy can be easily adopted for other choices of v_b . Then, it uses two different partitioning strategies for levels $v \leq v_b$ and $v > v_b$.

- 1) At levels $v \leq v_b$, each processor computes and stores the complete ray and near-field data for approximately N_g^v/N_p boxes. Moreover, each processor stores the geometry information related to spatial basis functions $\mathbf{S}_n(\mathbf{r})$ in its N_g^1/N_p boxes and their near-field pairs at level v=1. By doing so, the memory and computation loads are only partitioned along the spatial dimension.
- 2) At levels $v>v_b$, computation and storage of the ray data of one box are distributed among $N_r^v=\lfloor N_p/N_g^v\rfloor$ processors. Each processor is in charge of storing N_k^v/N_r^v angular samples of one box's ray data, hence the memory load is simultaneously partitioned along the spatial and angular dimensions. This memory partitioning leads to the following workload partitioning: each processor performs the translation operation for N_k^v/N_r^v angular samples and all, temporal samples of the ray data of one box; in contrast, each processor spherically interpolates/filters the ray data for $O(T^v/N_r^v)$ temporal samples and all angular samples of the ray data of one box. This approach ensures that computation load is simultaneously partitioned along the spatial and angular/temporal dimensions.

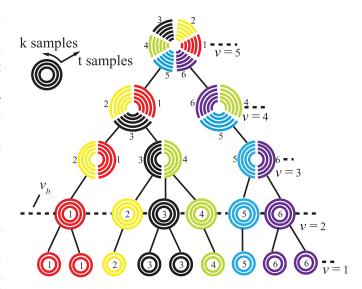


Fig. 1. Partitioning of boxes and their ray data in a five-level PWTD tree among six processors.

This partitioning strategy is perhaps best described by an example. Consider a five-level PWTD tree that is partitioned among six processors (Fig. 1). In Fig. 1, each set of concentric circles represents one box and its associated ray data. The angular and radial dimensions of the circles concern the angular and temporal samples of the ray data, respectively. The number printed near the concentric circles and arcs indicates the ID of the processor in charge of the data marked with a certain color. For this example, $N_g^v = 9, 6, 3, 2, 1$ for $v = 1, \ldots, 5$, and $N_p = 6$, therefore, $v_b = 2$.

First, the proposed strategy assigns each box at base level $v = v_b$ and with its corresponding subtree(s) to one processor. Each processor is responsible for computing and storing the ray data of the source/observer boxes at levels lower than the base level, i.e., $v \leq v_b$, which it is in charge of. In the example in Fig. 1, processor 1 is in charge of computing and storing the ray data of the leftmost two boxes at the first level and those of the single leftmost box at the second level. More specifically, processor 1 constructs/projects the ray data at the second and first levels by spherical interpolation/filtering of the ray data at the first and second level. Since the ray data at both levels are stored in processor 1, no interprocessor communication is needed. Each processor performs the translation stage without interprocessor communications if both source and observer boxes are handled by the same processor. Otherwise, it carries out the translation operation after receiving the outgoing ray data from other processors. Similarly, each processor is in charge of computing and storing the near-field data pertinent to source boxes at the finest level in its corresponding subtree. For example, in Fig. 1, processor 1 only computes and stores the near-field data pertinent to the leftmost two source boxes at the finest level. This near-field data are related to self and mutual interactions between the leftmost two source boxes and the mutual interaction between the second box (source box) and the third box (observer box) from the left. Note that in this example it is assumed that only adjacent (and self) boxes constitute near-field pairs. Since the near-field data pertinent to many box

pairs resides on the same processor, the communication cost for the near-field calculation is very low.

Second, the proposed strategy partitions the ray data of the boxes at levels higher than the base level, i.e., $v > v_b$, among processors by considering the number of processors and the number of boxes at that level. For example, in Fig. 1, the processor 1 is responsible for computing and storing 1/2, 1/3, and 1/6 of the ray data (i.e., $N_r^v = 2, 3, 6$) of the leftmost boxes at levels 3, 4, and 5, respectively. Translation of the ray data of one box is split among N_r^v processors in angular dimension. For example, processor 1 performs translation stage for half of the angular samples of the ray data of the leftmost box at level 3 after receiving from processor 6 the outgoing ray data of the rightmost box at that level. Note that in this example, it is assumed that, at level 3, only the leftmost and rightmost boxes constitute a far-field pair. In contrast, interpolating/filtering the ray data of one box is split among N_r^v processors in temporal dimension. For example, interpolating/filtering the ray data of the leftmost box at level 4 is carried out by processor 1, 2, and 3; the ray data are redistributed among these three processors in temporal dimension, each processor interpolates/filters 1/3 of the temporal samples of that box.

The proposed strategy gives rise to quasi-optimal (uniform) distribution of the ray data of boxes at each level of the PWTD tree as well as the near-field data among processors. Consequently, the memory requirement of PWTD-accelerated TD-SIE solver per processor scales as $O(N_s^{1.5}/N_p)$. In addition, this partitioning strategy produces a uniform distribution of the computation load among processors at each level of the PWTD tree. However, the sizes of communication packets, which scale as $O(N_k^v T^v/N_r^v) = O(2^v N_s/N_p)$, can be rather large at higher levels. A memory efficient and asynchronous communication scheme is, therefore, used to alleviate this high cost helping the proposed partitioning strategy produce scalable communication patterns. In the following sections, the computational and communication costs per processor at different stages of the multilevel PWTD algorithm are analyzed in detail.

2) Construction/Projection of Outgoing/Incoming Rays: The proposed strategy constructs the outgoing rays and projects the incoming rays separately for the finest level (v=1) and the other levels (v>1). Here, we only detail the procedures for constructing outgoing rays as those for the incoming rays are similar.

At level v=1, construction of outgoing rays proceeds directly from basis function data and requires no communication. At this level, construction of one outgoing ray for one box requires $O(T^1)$ operations. The computation of all ray data for one box $(N_l^1$ outgoing rays for N_k^v directions) requires $O(T^1N_l^1N_k^1)$ operations. As one processor is in charge of approximately N_g^1/N_p v=1 boxes, the computational cost per processor for constructing outgoing rays at this level scales as $O(T^1N_l^1N_k^1N_q^1/N_p) = O(N_tN_s/N_p)$.

At levels v > 1, construction of outgoing rays is carried out using spherical interpolation and may require interprocessor communication. Unlike the ray data of v = 1 boxes, that of each v > 1 box may be constructed and stored by more than one processor (see the example in Fig. 1). Analysis of the computational and communication costs associated with the construction of outgoing rays via spherical interpolation

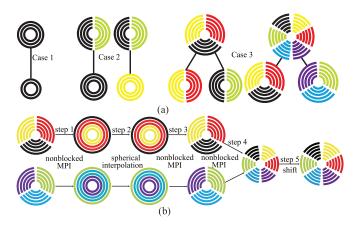


Fig. 2. (a) Three possible cases encountered during the construction of outgoing rays of boxes in PWTD tree. Each case requires different communication patterns. (b) Steps to construct the outgoing rays in case 3.

at levels v>1 requires consideration of three distinct cases [Fig. 2(a)].

- Case 1: Ray data of level v ≤ v_b boxes are directly interpolated and shifted from those of the child boxes, which the same the processor is in charge of. As the spherical interpolation of one temporal sample of ray data requires O(N_k^v log K^v) operations [4], [20], the computational cost of this operation scales as O(T^v N_k^v log K^v). This case requires no communication.
- 2) Case 2: Ray data of level $v = v_b + 1$ boxes are constructed by interpolating and shifting ray data of the child boxes, which are completely stored on another processor. The computational cost of this operation scales as $O(T^v N_k^v \log K^v)$. This case requires communication among processors following the interpolation step.
- 3) Case 3: Ray data of level $v > v_b + 1$ boxes are computed via interpolating and shifting the ray data of the child boxes, which invariably are stored on more than one processor. The construction of the outgoing rays [Fig. 2(b)] is performed in four steps. Step 1) Ray data of the child boxes stored in N_r^{v-1} processors is exchanged between them in such a way that each processor handles $O(T^v/N_r^{v-1})$ temporal samples of outgoing rays along all N_k^{v-1} directions [Fig. 2(b)]. Step 2) each processor performs its own spherical interpolation, requiring $O(T^v N_k^v \log K^v / N_r^{v-1})$ operations. Step 3) the interpolated ray data of each child box is split along the angular dimension and the resulting data is exchanged between N_r^{v-1} processors. Step 4) the interpolated ray data are sent to the processors in charge of the parent box via nonblocked MPI communication. Step 5) the transferred ray data are locally shifted to the center of the parent box.

For each processor, the computational cost CC_1 of constructing outgoing rays scales as

$$CC_{1} = O\left(\frac{N_{t}N_{s}}{N_{p}}\right) + \sum_{v=2}^{v_{b}+1} \frac{N_{g}^{v-1}}{N_{p}} N_{l}^{v} O\left(T^{v} N_{k}^{v} \log K^{v}\right) + \sum_{v=v_{b}+2}^{N_{L}} N_{l}^{v} O\left(\frac{T^{v} N_{k}^{v} \log K^{v}}{N_{r}^{v-1}}\right) = O\left(\frac{N_{t} N_{s} \log^{2} N_{s}}{N_{p}}\right).$$
(14)

In (14), three contributing terms represent the costs of constructing outgoing rays at the finest level, levels $2 < v \le v_b + 1$ (cases 1 and 2), and levels $v > v_b + 1$ (case 3), respectively. Note that each processor is in charge of (completely or partially) interpolating ray data of approximately N_g^{v-1}/N_p child boxes for cases 1 and 2, and at most one child box for case 3.

The communication cost per processor is proportional to the total amount of data that it sends and receives. The cost of communications required at this stage of the PWTD algorithm is dominated by the data exchange required in case 3 [depicted as Steps 1), 3), and 4) in Fig. 2(b)]. In Steps 1) and 3), each processor sends partial ray data of size $O(T^vN_k^v/N_r^{v-1})$ to $N_r^{v-1}-1$ other processors and receives data of size $O(T^vN_k^v/(N_r^{v-1})^2)$ from each of the other $N_r^{v-1}-1$ processors. Therefore, the total amount of data that one processor sends/receives in Steps 1) and 3) scales as $O(T^vN_k^v/N_r^{v-1})$. In Step 4), each processor sends/receives ray data in the amount of $O(T^vN_k^v/N_r^{v-1})$ to/from O(1) other processors. Therefore, the communication cost CM_1 in this stage scales as

$$CM_1 = \sum_{v=v_b+2}^{N_L} N_l^v O\left(\frac{T^v N_k^v}{N_r^{v-1}}\right) = O\left(\frac{N_s N_t \log N_p}{N_p}\right).$$

$$(15)$$

Note that each term in the summation in (15) scales as $O(N_s N_t/N_p)$ and the total number of terms is $N_L - v_b - 1 = O(\log N_p)$.

The computational cost CC_2 associated with projecting incoming rays onto observers and the cost of communications required by this operation CM_2 are proportional to those associated with the construction of outgoing rays, i.e.

$$CC_2 \propto CC_1, CM_2 \propto CM_1.$$
 (16)

3) Translation Stage: Translating one outgoing ray from a source box onto the incoming ray of an observer box requires the temporal convolution of the outgoing ray with the translation function in (13). This convolution is carried out in the Fourier domain, i.e., by inverse Fourier transforming the product of the ray's and translation function's Fourier transforms [2]. The computational cost associated with this operation scales as $O(T^v \log T^v)$. Note that for a level- v source box there exist N_l^v outgoing rays along each direction, and that each observer box interacts with O(1) source boxes. At levels $v \leq v_b$, each processor performs translations along N_k^v directions for its N_a^v/N_p observer boxes; at levels $v > v_b$, each processor performs translations along N_k^v/N_r^v directions for one single observer box. The computational cost of the translation operation CC_3 for each processor can, therefore, be estimated as

$$CC_{3} = \sum_{v=1}^{v_{b}} \frac{N_{g}^{v}}{N_{p}} N_{l}^{v} O(1) N_{k}^{v} O(T^{v} \log T^{v})$$

$$+ \sum_{v=v_{b}+1}^{N_{L}} N_{l}^{v} O(1) \frac{N_{k}^{v}}{N_{r}^{v}} O(T^{v} \log T^{v})$$

$$= O\left(\frac{N_{t} N_{s} \log^{2} N_{s}}{N_{p}}\right). \tag{17}$$

Here, the first and second summations represent costs of translation operations at levels $v \le v_b$ and $v > v_b$, respectively.

Note that during the translation stage outgoing ray data of a box residing in a processor is sent to at most O(1) other processors. In fact, some translations do not require communications whatsoever since they involve source and observer boxes residing on the same processor. For each processor, the amount of ray data sent and received during the translation stage for one box scales as $O(N_k^v T^v)$ for levels $v \leq v_b$ and $O(N_k^v T^v/N_r^v)$ for levels $v > v_b$. Therefore, the communication cost for the translation stage CM_3 scales as

$$CM_{3} = \sum_{v=1}^{v_{b}} \frac{N_{g}^{v}}{N_{p}} N_{l}^{v} O\left(N_{k}^{v} T^{v}\right) + \sum_{v=v_{b}+1}^{N_{L}} N_{l}^{v} O\left(\frac{N_{k}^{v} T^{v}}{N_{r}^{v}}\right)$$
$$= O\left(\frac{N_{t} N_{s} \log N_{s}}{N_{p}}\right). \tag{18}$$

Oftentimes, the number of source boxes far-field paired with one observer box is large (e.g., exceeds one hundred) and the processor in charge of the observer box needs to allocate temporary memory for receiving all outgoing rays of source boxes it is interacting with. This temporary memory space may become excessively large, especially for translations at higher levels. To overcome this potential bottleneck, a novel, memory-efficient, and asynchronous communication scheme is proposed here. This scheme leverages concepts originally proposed in [21]. It no longer separates the computation and communication phases and limits the temporary memory to be allocated to that available to a single processor. The workflow of this scheme for one processor can be summarized as follows (Fig. 3). First, the processor allocates a "receiving" memory pool containing memory grains of size $O(T^v N_k^v)$ or $O(T^v N_k^v / N_r^v)$ to receive outgoing rays of one source box at level $v \le v_b$ or $v > v_b$, respectively. Prior to translation, the processor sends out all/partial outgoing rays of source boxes needed by the far-field observer boxes that are held by different processors. The processor then iterates over the following four steps until the translation stage is complete. Step 1) The processor catches arriving data packets (i.e., outgoing ray data from source boxes). If a memory grain is available, the processor starts receiving the packet by putting it into the receiving queue. If not, it temporarily suspends reception of the packet until the next iteration. Step 2) The processor moves any completed packets in the receiving queue into a working queue, which now contains packets that are complete for translation. Step 3) The processor carries out the translation of complete outgoing rays. The working queue is a "priority queue" such that translations associated with "nonlocal" packets (as opposed to "local" packets that require no temporary memory space) and "higher level" packets that correspond to outgoing rays of boxes at higher levels, are executed first. Step 4) After translation, the memory grain associated with the packet is returned to the pool and becomes available again for Step 1). In this manner, the translation and communication are performed asynchronously and the maximum amount of temporary memory allocated is fully controlled by the processor.

4) Near-Field Calculation: Near-field calculations include: 1) matrix–vector multiplications on the left-hand side (LHS)

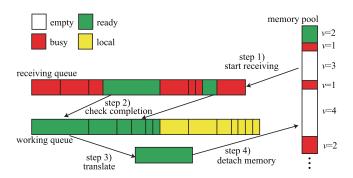


Fig. 3. Queue-based asynchronous communication during translation stage.

of (7) at each iteration of the solver executed for solving (7) at all time steps; and 2) partial matrix–vector multiplications on the RHS of (7). Here, it is assumed that the solution of (7) requires O(1) iterations, a condition that typically is satisfied for nonresonant objects under high-frequency illumination. Since each box only participates in O(1) near-field pairs, the near-field computational and communication costs per box scale as $O((N_s/N_g^1)^2) = O(1)$ and $O(N_s/N_g^1) = O(1)$, respectively. As one processor is in charge of approximately N_g^1/N_p source boxes at the finest level, the computational cost and communication cost of the near-field calculations CC_4 and CM_4 scale as

$$CC_4 \propto CM_4 = \frac{N_g^1}{N_p} N_t O\left(1\right) = O\left(\frac{N_s N_t}{N_p}\right).$$
 (19)

5) Total Computational and Communication Costs: By summing the computational and communication costs in (14)–(19) and noting that CC_4 is bounded by $CC_{\{1,2,3\}}$ and $CM_{\{1,4\}}$ is bounded by $CM_{\{2,3\}}$, the total computational cost and total communication cost of the PWTD-accelerated TD-SIE solver CM and CC scale as

$$CC = O\left(\frac{N_t N_s \log^2 N_s}{N_p}\right), \quad CM = O\left(\frac{N_t N_s \log N_s}{N_p}\right).$$
 (20)

These costs are inversely proportional to the number of processors N_p . As the memory requirements of the solver $O(N_s^{1.5}/N_p)$ are also inversely proportional to N_p , it can be concluded that the proposed parallel PWTD-accelerated TD-SIE solver is scalable.

III. NUMERICAL RESULTS

This section presents numerical examples that demonstrate the efficiency, accuracy, and applicability of the proposed parallel PWTD-accelerated TD-SIE solver. In all examples considered here, the scatterers are excited by a plane wave with electric field given by

$$\mathbf{E}^{i}(\mathbf{r},t) = \hat{\mathbf{p}}G(t - \mathbf{r} \cdot \hat{\mathbf{k}}/c_{0}). \tag{21}$$

 $G(t)=\cos(2\pi f_0(t-t_0))\exp(-(t-t_0)^2/2\sigma^2)$ is a modulated and quasi-bandlimited Gaussian pulse, f_0 is the modulation frequency, $t_0=6\sigma$ is the delay, $\sigma=3/(2\pi f_{bw})$ is a

measure of pulse duration, and $\hat{\mathbf{p}}$ and $\hat{\mathbf{k}}$ denote the polarization and propagation direction of the plane wave. The parameter f_{bw} represents the "half bandwidth" of the pulse. The minimum and maximum frequencies are $f_{min} = f_0 - f_{bw}$ and $f_{max} = f_0 + f_{bw}$; energy outside this frequency band is only 0.0022% of the pulse's total energy. A parallel generalized minimal residual (GMRES) algorithm and a diagonal preconditioner are used to iteratively solve (7) at each time step. The GMRES iteration is terminated when the condition

$$\|\bar{\mathbf{V}}_j - \bar{\bar{\mathbf{Z}}}_0 \bar{\mathbf{I}}_i^{(n)}\| < \varepsilon \|\bar{\mathbf{V}}_j\| \tag{22}$$

is satisfied. Here, $\bar{\mathbf{I}}_j^{(n)}$ represents the vector of current coefficients in the n^{th} iteration, $\bar{\mathbf{V}}_j = \bar{\mathbf{F}}_j - \sum_{i=1}^{j-1} \bar{\bar{\mathbf{Z}}}_i \bar{\mathbf{I}}_{j-i}$ is total RHS at time step j, and ε is the desired residual error. At a given time step, the GMRES solver's initial guess is constructed by extrapolating current coefficients from those obtained in previous time steps. All frequency-domain (i.e., time harmonic) data presented in this section are obtained by dividing the Fourier transform of the time-domain waveforms (whose samples are recorded during MOT) by that of G(t).

All simulations were executed on a cluster of Quad-Core 850 MHz PowerPC CPUs with 4 GB/CPU memory, which is located at the King Abdullah University of Science and Technology (KAUST) Supercomputing Laboratory. The proposed scheme solved either the TD-CFIE ($\beta=1$) or TD-EFIE leveraging a hybrid MPI and OpenMP parallelization strategy: one MPI process was launched per CPU and OpenMP utilized four cores on each CPU to parallelize the computationally most intensive loops associated with translation, near-field calculation, and construction/projection of outgoing/incoming rays at level v=1.

A. Parallelization Performance and Solution for Canonical Problems

1) Plate: First, parallelization performance of the PWTD-accelerated TD-EFIE solver is demonstrated by analyzing scattering from a 120 m \times 120 m PEC plate. The plate is centered at the origin and positioned parallel to the xy plane. It is illuminated by $\mathbf{E}^i(\mathbf{r},t)$ in (21) with $f_0=150\,\mathrm{MHz},$ $f_{bw}=50\,\mathrm{MHz},$ $\hat{\mathbf{p}}=\hat{\mathbf{z}},$ and $\hat{\mathbf{k}}=\hat{\mathbf{y}}.$ The current induced on the plate is discretized using $N_s=731,247$ spatial basis functions and fourth-order temporal basis function. The simulation is executed for $N_t=500$ time steps with $\Delta t=250\,\mathrm{ps}.$ A nine-level PWTD tree is constructed upon setting the side length of boxes at the finest level to 0.64λ and $\gamma=3.$ The number of boxes at each level is $N_g^v=\{65\,536,16\,384,4096,1024,256,64,16,4,1\}.$

The parallel efficiencies are computed using $\kappa = N_{ref}T_{N_{ref}}/N_pT_{N_p}$, where $T_{N_{ref}}$ and T_{N_p} are the measured execution times (including computation and communication times) on N_{ref} and N_p processors, respectively. Note that N_{ref} is chosen as the minimum number of processors (usually greater than one) required to execute the simulation in a reasonable time. This is a valid measure of the *true* parallel efficiency as the efficiency only starts to (noticeably) degrade when N_p becomes very large. κ is computed for different PWTD

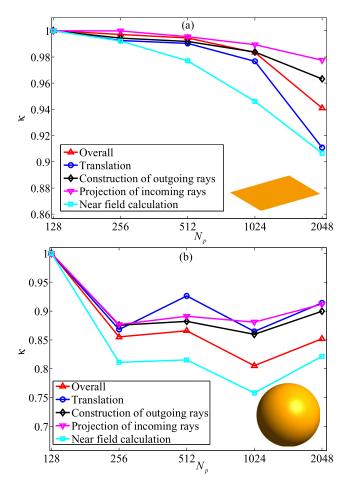


Fig. 4. Parallel efficiencies (κ) of PWTD stages for the canonical problem involving (a) square PEC plate and (b) PEC sphere while N_p is changed from 128 to 2048 $(N_{ref}=128)$.

stages for $N_p = \{128, 256, 512, 1024, 2048\}$ and $N_{ref} = 128$ [Fig. 4(a)]. The base level is set to $v_b = \{4, 4, 3, 3, 2\}$, respectively. As discussed in Section II-C2, three different cases exist when constructing/projecting outgoing/incoming rays. For case 1, parallel efficiency is 100% since there is no communication between processors. As expected, the same efficiency cannot be achieved for cases 2 and 3. However, still, over 96% overall parallel efficiency is achieved for construction/projection of outgoing/incoming rays even when $N_p = 2048$. Moreover, efficiencies of 90% and 91% are achieved for the near-field calculation and the translation stages. Note that lower efficiency is observed for the near-field calculation stage compared to the other stages. This is due to nonideal load balancing when each processor is in charge of different numbers of finest level boxes, especially if the subtree has many levels. Nevertheless, the overall solver achieves 94% parallel efficiency (i.e., 15-fold speedup) when $N_p = 2048$. It is worth pointing out that this high efficiency may demonstrate the scalability in (20) as it is difficult to separately measure the computation/communication time due to extensive usage of nonblocked and asynchronous communication techniques. Fig. 5(a) plots the execution time on each processor when $N_p = 2048$; excellent computation load balance is observed. Moreover, the maximum memory usages among all processors when $N_p=128$ and $N_p=2048$

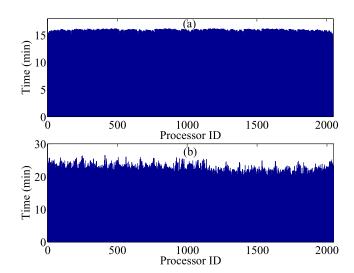


Fig. 5. Execution time on each processor for the canonical problem involving (a) square PEC plate and (b) PEC sphere when $N_p=2048.$

TABLE I MAXIMUM MEMORY COSTS AMONG ALL PROCESSORS FOR THE CANONICAL PROBLEM INVOLVING A SQUARE PEC PLATE AND A PEC SPHERE WHEN $N_p=128~{\rm And}~2048$

	PWTD (plate)	Near-field (plate)	PWTD (sphere)	Near-field (sphere)
$N_p = 128$	1.96 GB	671.7 MB	388 MB	1.45 GB
$N_p = 2048$	135 MB	47.6 MB	26.3 MB	109.5 MB
Ratio	14.8	14.1	14.7	13.5

are listed in Table I; memory reduction ratios of 14.8 and 14.1 are achieved (compared to the ideal reduction ratio of 16) for the storage of the PWTD ray data and the near-field data, respectively. Note that lower memory reduction ratio is observed for the near-field data compared to the PWTD ray data, due to similar reasons causing the lower parallel efficiency for the near-field calculation.

The PWTD-accelerated TD-EFIE solver is then applied to the analysis of scattering from the PEC plate illuminated by $\mathbf{E}^i(\mathbf{r},t)$ in (21) with $f_0=300$ MHz, $f_{bw}=100$ MHz, $\hat{\mathbf{p}}=-\hat{\mathbf{y}}$, and $\hat{\mathbf{k}}=-\sin(5^\circ)\hat{\mathbf{x}}-\cos(5^\circ)\hat{\mathbf{z}}$. The current induced on the plate is discretized using $N_s=2,920,476$ spatial basis functions and fourth-order temporal basis function. The simulation is executed for $N_t=1000$ time steps with $\Delta t=125$ ps. A tenlevel PWTD tree is constructed upon setting the side length of boxes at the finest level to 0.64λ and $\gamma=3.5$. Table II presents the solver parameters and CPU and memory requirements of the solver's different stages. The solver requires 3.5 TB of memory and 7 h of CPU time when $N_p=2048$. The solution at each time step is obtained in just one GMRES iteration, which yields $\varepsilon=10^{-12}$.

The broadband RCS of the plate along the +z direction ($\theta = 0$) obtained using the proposed solver is compared the approximate analytical solution (which only accounts for physical optics) in Fig. 6; good agreement is observed.

2) Sphere: Next, parallelization performance of the PWTD-accelerated TD-CFIE solver is demonstrated via the analysis of scattering from a PEC sphere of radius 1 m

TABLE II				
TECHNICAL DATA FOR THE SETUPS AND SOLUTIONS OF SCATTERING				
PROBLEMS INVOLVING CANONICAL EXAMPLES				

	Plate	Sphere
Maximum dimension	120 m (160 λ)	2 m (70 λ)
Frequency band (f_{min}, f_{max})	(200,400) MHz	(5.12,10.24) GHz
Number of unknowns N_s	2 920 476	9 433 437
Time step size Δt	125 ps	5 ps
Number of time steps N,	1 000	2 300
Number of processors N_p	2 048	8 192
Memory for $\overline{\overline{\mathbf{Z}}}_i$ /PWTD	0.46/3.02 TB	3.28/3.78 TB
Setup time	1.63 h	3 h
RHS time (near field)	628 s	1.38 h
RHS time (PWTD)	4 h	11.2 h
LHS time (GMRES)	108 s	3.8 h
Number of GMRES iterations	1	1

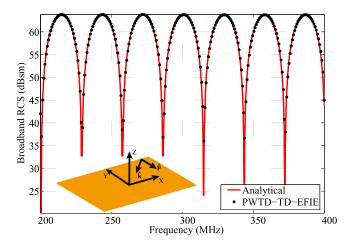


Fig. 6. broadband RCS of the PEC plate along $\pm z$ direction computed by the PWTD-accelerated TD-EFIE solver and analytical formula.

centered at the origin. It is illuminated by $\mathbf{E}^i(\mathbf{r},t)$ in (21) with $f_0=2.3 \mathrm{GHz}, f_{bw}=0.77 \mathrm{GHz}, \hat{\mathbf{p}}=\hat{\mathbf{x}},$ and $\hat{\mathbf{k}}=\hat{\mathbf{z}}.$ The current induced on the sphere is discretized using $N_s=992,766$ spatial basis functions and fourth-order temporal basis function. The simulation is executed for $N_t=900$ time steps with $\Delta t=16.7\,\mathrm{ps}.$ A seven-level PWTD tree is constructed upon setting the side length of boxes at the finest level to 0.479λ and $\gamma=3.5.$ The number of boxes at each level is $N_q^v=\{18481,4739,1160,272,56,8,1\}.$

The parallel efficiency κ is computed for different PWTD stages for $N_p = \{128, 256, 512, 1024, 2048\}$ and $N_{ref} = 128$ [Fig. 4(b)]. The base level is set to $v_b = \{3, 3, 2, 2, 1\}$, respectively. Fig. 4(b) shows that there is a higher efficiency degradation in comparison to efficiency curves presented in Fig. 4(a). This is due to load imbalance, which is observed because the number of boxes at each level is not an integer power of two. For the same reason, the efficiencies decrease in a "staircase" fashion and exhibit noticeable local increases whenever v_b is incremented [Fig. 4(b)]. That said, the overall parallel efficiency is still rather high around 85% (corresponding to 13.6-fold speedup) even when $N_p = 2048$. Fig. 5(b) plots the execution time on each processor when $N_p = 2048$. For similar

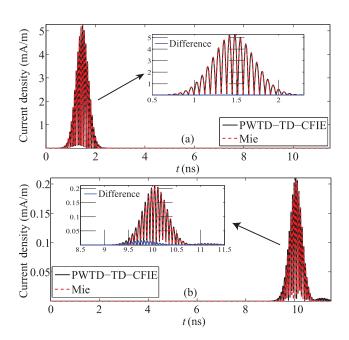


Fig. 7. Magnitudes of the current density induced at (a) $(r=1, \theta=180^{\circ}, \phi=0)$ and (b) $(r=1, \theta=0, \phi=0)$ on the sphere computed using the proposed solver and MIE series. Note that the maximum current magnitude at (b) is 25 times smaller than that at (a).

TABLE III
THE TECHNICAL DATA FOR THE SETUPS AND SOLUTIONS OF
SCATTERING PROBLEMS INVOLVING REAL-LIFE TARGETS

	Helicopter	Airbus A-320
Maximum dimension	22 m (117.3 λ)	37.5 m (33 λ)
Frequency band (f_{min}, f_{max})	(0.8,1.6) GHz	(133.5, 266.5) MHz
Number of unknowns N_s	2 436 813	1 020 069
Time step size Δt	31.25 ps	187 ps
Number of time steps N _t	3 000	1 600
Number of processors N_p	8 192	2 048
Memory for $\overline{\overline{\mathbf{Z}}}_i$ /PWTD	907/820 GB	1152/70 GB
Setup time	2.8 h	5.6 h
RHS time (near field)	42 min	3 h
RHS time (PWTD)	14 h	2.7 h
LHS time (GMRES)	21 min	800 s
Number of GMRES iterations	1–3	2–3
RCS calculation time	2.53 h	55 min

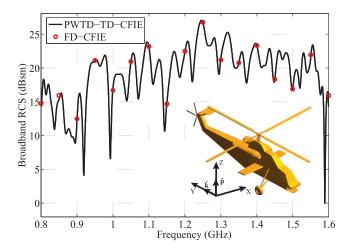


Fig. 8. Broadband RCS of the Rooivalk helicopter along the $\pm z$ direction computed by PWTD-accelerated TD-CFIE solver and FD-CFIE solver.

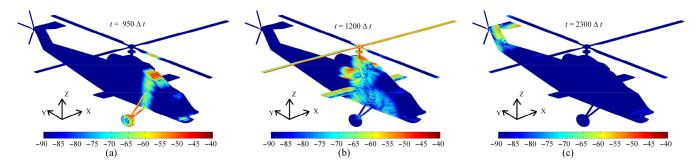


Fig. 9. Snapshots of the current density (in dB) induced on the helicopter obtained by the PWTD-accelerated TD-CFIE solver at (a) $t=950~\Delta t$, (b) $t=1200~\Delta t$, and (c) $t=2300~\Delta t$.

reasons as explained above, nonideal computation load balance is observed in Fig. 5(b) compared with that in Fig. 5(a). Moreover, the maximum memory usages among all processors when $N_p=128$ and $N_p=2048$ are listed in Table I; memory reduction ratios of 14.7 and 13.5 are achieved (compared to the ideal reduction ratio of 16) for the storage of the PWTD ray data and the near-field data, respectively.

The PWTD-accelerated TD-CFIE solver is then applied to the analysis of scattering from the PEC sphere illuminated by $\mathbf{E}^i(\mathbf{r},t)$ in (21) with $f_0=7.68$ GHz, $f_{bw}=2.56$ GHz, $\hat{\mathbf{p}}=\hat{\mathbf{x}}$, and $\hat{\mathbf{k}}=\hat{\mathbf{z}}$. The current induced on the sphere is discretized using $N_s=9,433,437$ spatial basis functions and fourth-order temporal basis function. The simulation is executed for $N_t=2,300$ time steps with $\Delta t=5$ ps. A nine-level PWTD tree is constructed upon setting the side length of boxes at the finest level to 0.404λ and $\gamma=4.5$. Table II presents the solver parameters and CPU and memory requirements of the solver's different stages. The solver requires 7 TB of memory and 20 h of CPU time when $N_p=8192$. The solution at each time step is obtained in just one GMRES iteration, which yields $\varepsilon=10^{-12}$.

Current densities induced at $(\theta = 180^{\circ}, \phi = 0)$ and $(\theta = 0, \phi = 0)$ obtained using the proposed solver are compared with the exact Mie series solution in Fig. 7.

B. Solution for Real-World Problems

1) Rooivalk Helicopter: The PWTD-accelerated TD-CFIE solver is applied to the analysis of scattering from a Rooivalk helicopter model, which fits in a fictitious box of dimensions $7.4 \,\mathrm{m} \times 22 \,\mathrm{m} \times 7.1 \,\mathrm{m}$. The helicopter is illuminated by $\mathbf{E}^i(\mathbf{r},t)$ in (21) with $f_0 = 1.2 \,\text{GHz}$, $f_{bw} = 0.4 \,\text{GHz}$, $\hat{\mathbf{p}} = \hat{\mathbf{z}}$, and $\hat{\mathbf{k}} =$ ŷ. The current induced on the helicopter is discretized using $N_s = 2,436,813$ spatial basis functions and fourth-order temporal basis function. The simulation is executed for $N_t = 3000$ time steps with $\Delta t = 31.25 \, \mathrm{ps.}$ A ten-level PWTD tree is constructed upon setting the side length of boxes at the finest level to 0.48λ and $\gamma = 4$. Table III presents the solver parameters and CPU and memory requirements of the solver's different stages. The solver requires around 2 TB of memory and 21 h of CPU time when $N_p = 8192$. The solution at each time step is obtained in maximum three GMRES iterations, which yields $\varepsilon = 5 \times 10^{-14}$.

The broadband RCS of the helicopter along the +z direction ($\theta=0$) is computed using the proposed solver and a

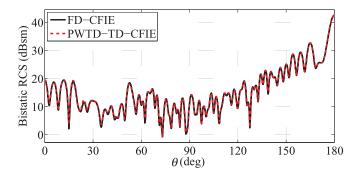


Fig. 10. Bistatic RCS of the Airbus-A320 airplane at 250 MHz computed at $\phi=0^\circ$ and $\theta=[0,180]^\circ$ via PWTD-accelerated TD-CFIE solver and FD-CFIE solver.

frequency-domain combined field integral equation (FD-CFIE) solver (Fig. 8). Results are in good agreement. Moreover, snapshots of the current induced on the helicopter at times $950~\Delta t, 1200~\Delta t,$ and $2300~\Delta t$ are shown in Fig. 9. The amplitudes of currents on edges and regions where multiple reflections exist are larger than those elsewhere.

2) Airbus-A320: Finally, the PWTD-accelerated PWTD solver is applied to the analysis of transient scattering from an Airbus-A320 model, which fits in a hypothetical box with dimensions $34.2 \,\mathrm{m} \times 11.7 \,\mathrm{m} \times 37.5 \,\mathrm{m}$. The airplane is illuminated by $\mathbf{E}^{i}(\mathbf{r}, t)$ in (21) with $f_{0} = 200 \,\text{MHz}, f_{bw} = 67.5 \,\text{MHz},$ $\hat{\mathbf{p}} = \hat{\mathbf{y}}$, and $\hat{\mathbf{k}} = -\hat{\mathbf{z}}$. The current induced on the airplane is discretized using $N_s = 1,020,069$ spatial basis functions and fourth-order temporal basis function. The simulation is executed for $N_t = 1600$ time steps with $\Delta t = 187$ ps. An eightlevel PWTD tree is constructed upon setting the side length of boxes at the finest level to 0.38 λ and $\gamma = 4.5$. Table III presents the solver parameters and CPU and memory requirements of the solver's different stages. The solver requires about 1.2 TB of memory and 12.6 h of CPU time when $N_p = 2048$. The solution at each time step is obtained in maximum three GMRES iterations, which yields $\varepsilon = 5 \times 10^{-15}$.

The bistatic RCS of the airplane is computed at 250 MHz and compared to those obtained using a FD-CFIE solver (Fig. 10); results are in good agreement. In addition, snapshots of the current induced on the airplane at times $640~\Delta t,~800~\Delta t,~$ and $1140~\Delta t$ are shown in Fig. 11. The maximum current densities are induced on edges of antennas placed on top of the airplane and the engine intake.

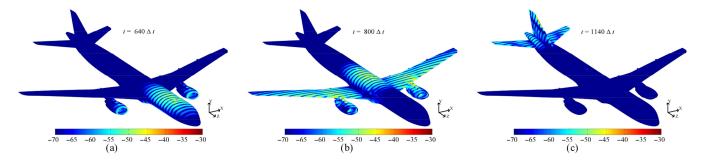


Fig. 11. Snapshots of the current density (in dB) induced on the Airbus-A320 obtained by the PWTD-accelerated TD-CFIE solver at (a) $t = 640 \ \Delta t$, (b) $t = 800 \ \Delta t$, and (c) $t = 1140 \ \Delta t$.

IV. CONCLUSION

A scheme for efficiently parallelizing PWTD-accelerated TD-SIE solvers is presented. Its efficiency is achieved by hierarchically partitioning the computation and memory loads along the spatial, angular, and temporal dimensions. The resulting partitioned memory, CPU, and communication costs scale as $O(N_s^{1.5}/N_p)$, $O(N_tN_s\log^2N_s/N_p)$, and $O(N_t N_s \log N_s / N_p)$, respectively. Furthermore, to enable scalable communications among processors, the parallelization scheme employs a mix of nonblocked MPI and queuebased, memory-efficient, and asynchronous communication techniques. Indeed, numerical results validate the scalability and parallel efficiency of the proposed scheme up to 2048 processors. The resulting PWTD-accelerated TD-SIE solver is successfully applied to analysis of transient scattering from objects measuring well over 100 wavelengths in size and discretized using 10 million spatial unknowns.

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